

PATENT APPLICATION Attorney's Do. No. 9898-204

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Jae-Yoon SIM and Jei-Hwan YOO

Serial No.

09/901,930

Examiner:

Not yet assigned

Filed:

July 9, 2001

Group Art Unit:

2204

For:

NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR

MEMORY DEVICE

TRANSMITTAL LETTER

Assistant Commissioner for Patents Washington, D.C. 20231

Enclosed for filing in the above-referenced application are the following:

Request to Correction for Filing Receipt (including exhibits A-C)

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20575

PATENT TRADEMARK OFFICE

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

James G. Stewart Reg. No. 32,496

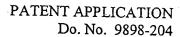
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I HEREBY CERTIFY THAT THIS COR-RESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSIAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: INCOMMISSIONER OF PATENTS AND TRADEMARKS, WASHINGTON D.C. 20231

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BOX NON FEE AMENDMENT Assistant Commissioner for Patents Washington, D.C. 20231



PRELIMINARY AMENDMENT

Please amend the application as follows.

IN THE SPECIFICATION

Please replace the paragraph beginning on page 1 at line 3 with the following:

This application claims priority from Provisional Application No. 60/288,746, entitled NEGATIVE VOLTAGE GENERATOR IN SEMICONDUCTOR MEMORY DEVICE, filed on May 4, 2001, by the same inventors of the present application, the contents of which are herein incorporated by reference.

